## **Curriculum Vitae**

# [Meléndez, Juan]

#### **CURRENT POSITION**

Since 2017 - CEIT

**General Manager** 

#### PROFESSIONAL BACKGROUND

2015-2017 CEIT - Transport and energy division director

2015-2017 CEIT – Railway group director

2011-2015 CEIT – Development Area Manager

2003–2011 CEIT – Head of Embedded System research line

2003–2002 CEIT – Project Manager

1998-2002 CEIT - PhD. student

### **EDUCATION AND TRAININGS:**

2011 - IESE Business School - Program for Directives Development (PDD)

2008 - Universidad Autónoma de Madrid - Reliability in electronic design (FIABET)

2003 – Universidad Politécnica de Cataluña – Master in Six Sigma Production Processes

2002 - University of Navarra - PhD. in Industrial Engineering

1998 – Fraunhofer Institut fur Integrierte Schaltungen – Master Thesis

1992 - 1998 - University of Navarra - Industrial engineering

#### **EXTRAS:**

Dr. Meléndez is co-author of two books related with integrated circuit design.

Dr. Meléndez has contributed to more than 20 publications in technical journals and international congresses.